

AF/2800

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Zhongze Wang et al.

Title: METHOD TO REDUCE TRANSISTOR CHANNEL LENGTH USING SDOX

Docket No.: 303.747US1

Serial No.: 09/810,005

Filed: March 16, 2001

Due Date: February 21, 2003

Examiner: Samuel A. Gebremariam

Group Art Unit: 2811

Box AF

Commissioner for Patents
Washington, D.C. 20231

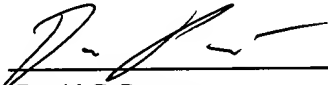
We are transmitting herewith the following attached items (as indicated with an "X"):

- ☒ A return postcard.
- ☒ An Amendment and Response under 37 § 1.116 (11 Pages).
- ☒ Clean Version of Pending Claims (10 pgs.).

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CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Box AF, Commissioner for Patents, Washington, D.C. 20231, on this 21st day of January, 2003.

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